



Layer Stack Up Detail for: Safety\_Board\_PCB\_Rev\_D.PcbDoc

Layer Name	ANALOG	DIGITAL Areas
L1-TOP	Component/Routing	Component/Routing
L2-Plane	AGND	Various GND
L3-Signal	Routing	Routing
L4-Signal	-15V Plane (pour)	Routing
L5-Plane	+15V	+5V
L6-BOT	Component/Routing	Component/Routing

		<b>University of Hawaii Institute for Astronomy</b>	
		DWG # <b>TC3-2072</b>	REV <b>D</b>
ENGINEER E. Wambier	FILE NAME: Safety_Board_PCB_Rev_D_Fab_Des.PcbDoc	SIZE <b>B</b>	SHEET <b>1 of 2</b>

