**WARNING: BOARD CONTAINS 208 VAC CIRCUITRY! BE CAREFUL.**

1. Unless otherwise stated, Resistors are 250 mW, 1% tolerance. Capacitors are 50V, 10% tolerance.

2. OPA4277UA do not require bias cancelling resistors.

**NOTE:** Design error - logic inverted at Q3 base.

- Digital input is a photodiode with 2.2k resistor (optocoupler).
  - A/D will be 0-10V (42k input impedance).

- Pocket PLC Output is setup as follows:
  - Digital output is +24V or 10k to GND.
  - D/A is 0-10V for actuator, +/-10V for Display.

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**NOTE:** Timer is about 60 seconds.

**NOTE:** Power & I/O connections to Pocket PLC.

**NOTE:** Power & I/O connections to Pocket PLC.
NOTE: thermistor is 10k.

NOTE: CMD (DAC output) is 0 to 10 V.

DA (0-10) x Gain (1.5) allows ~0.3V (amp headroom) to 15V output for margin.